

# Demands built on experience



The NCAB Group's standard specifications for PCBs requirements stretches to 31 pages, covering more than 100 separate criteria. These have been formulated during the 20 years the company has been producing PCBs, primarily in Asia. The product specification has taken shape in an ongoing process of improvement. This has included taking into account every fault or deviation reported by our customers as a starting point for further improving our product.

A competent specification results in a better PCB, leading to lower costs for our customers, given the considerable costs that can arise from faults during production and particularly in the field.

Several of our requirements significantly exceed those stated in IPC Class 2. As we see it, the IPC requirements have been designed to meet the needs of a broad range of product and industry sectors. NCAB's customers often demand considerably higher standards, with many of their products having a minimum 20-year life cycle, are often manufactured or assembled in high-cost countries, where faults or stoppages can prove extremely costly.

We have compiled a list of 14 crucial requirements in the production of PCBs. We have also indicated which of these are "Beyond IPC", i.e. stricter than the IPC requirements.

We have highlighted those requirements, which in our opinion as a PCB producer, we consider to be unique to the NCAB Group.

Our PCB specification is a dynamic document, part of our ongoing improvement process in which we actively seek input by our team of 30 technicians as well as our customers.

## NCAB GROUP PCB SPECIFICATION, BEYOND IPC CLASS 2

# The 14 most important features for a durable PCB

## 01 25 micron nominal hole plating as per IPC class 3

**BENEFITS**

Increased reliability including improved z-axis expansion resistance.

**RISK OF NOT HAVING**

Blow holes or outgassing, electrical continuity problems (inner layer separation, barrel cracking) during assembly or risk of field failures under load conditions. IPC Class 2 (standard for most factories) provides 20% less copper.



## 03 Cleanliness requirements beyond those of IPC

**BENEFITS**

Improved cleanliness of the PCB influences increased reliability.

**RISK OF NOT HAVING**

Residues on the boards, solder pick up, risk of conformal coating problems, ionic residues leading to risk of corrosion and contamination of the surfaces which are used for soldering - both potentially leading to reliability issues (poor solder joint / electrical failures) and ultimately increased potential for field failures.



## 02 No track welding or open circuit repair

**BENEFITS**

Reliability through perfect circuitry and security as no repair = no risk.

**RISK OF NOT HAVING**

Poor repair can actually lead to open circuits being supplied. Even a 'good' repair has a risk of failure under load conditions (vibration etc.) leading to potential field failures.



## 04 Tight control on age of specific finishes

**BENEFITS**

Solderability, reliability and less risk of moisture ingress.

**RISK OF NOT HAVING**

Solderability problems can occur as a result of metallurgical changes within the finish of old boards, whilst moisture ingress can lead to delamination, inner layer separation (open circuits) during assembly and/or when in the field.



## 05 Internationally known base materials used – no ‘local’ or unknown brands allowed



### BENEFITS

Increased reliability and known performance.

### RISK OF NOT HAVING

Poor mechanical properties mean the board doesn’t behave as expected during assembly conditions – for example: higher expansion properties leading to delamination / open circuits and also warpage problems. Reduced electrical characteristics can lead to poor impedance performance.

## 08 Defined tolerances for profile, holes and other mechanical features



### BENEFITS

Tighter tolerances means improved dimensional quality of the product – better fit, form and function.

### RISK OF NOT HAVING

Problems during assembly such as alignment / fit (press fit pin problems that are only found when the unit is fully assembled). Also problems with assembly into any housing due to increased deviation in dimensions.

## 06 Tolerance for copper clad laminate is IPC4101 class B/L



### BENEFITS

Tighter control of dielectric spacing provides less deviation in electrical performance expectations.

### RISK OF NOT HAVING

Electrical characteristics may not be exactly as planned and units within the same batch can demonstrate greater variation in output / performance.

## 09 NCAB Group specifies soldermask thickness – IPC does not



### BENEFITS

Better electrical insulation, less risk of flaking or loss adhesion and greater resilient to mechanical impact – wherever that may happen!

### RISK OF NOT HAVING

Thin deposits of soldermask can lead to problems with adhesion, resistance to solvents and hardness – all of which can see soldermask coming away from the board ultimately leading to corrosion of copper circuitry. Poor insulation characteristics due to the thin deposit can lead to short circuits through unwanted electrical continuity / arcing.

## 07 Defined soldermasks and ensuring accordance to IPC-SM-840 class T

### BENEFITS

NCAB Group approves ‘good’ materials to provide security in the ink and in knowing the soldermasks are covered within UL approvals.

### RISK OF NOT HAVING

Poor inks can lead to problems with adhesion, resistance to solvents and hardness – all of which can see soldermask coming away from the board ultimately leading to corrosion of the copper circuitry. Poor insulation characteristics can lead to short circuits through unwanted electrical continuity / arcing.

## 10 NCAB Group defines cosmetic and repair requirements – IPC does not



### BENEFITS

Security as a result of love and care during the manufacturing process.

### RISK OF NOT HAVING

Multiple scratches, minor damage, touch ups and repairs – a functional but perhaps unsightly board. If concerned over what can be seen, then what risks are involved with what cannot be seen, and the potential impact on assembly or risk when in the field?

## 11 Specific requirements of depth of via fill

### BENEFITS

A good quality filled via hole will provide less risk of rejection during the assembly process.

### RISK OF NOT HAVING

Half filled via holes may trap chemical residues from the ENIG process which can cause problems such as solderability. Such via holes can also trap solderballs within the hole which can escape and cause short circuits either during assembly or in the field.



## 13 NCAB Group specific qualification and release process for every purchase order

### BENEFITS

Security in knowing that through the release process, all of the specifications have been verified.

### RISK OF NOT HAVING

Risk that the product received will not be validated adequately and that any deviations to specification may not be spotted until assembly or final box build.... when it is too late.

## 12 Peters SD2955 peelable as standard

### BENEFITS

The benchmark for peelable mask – no 'local' or cheap brands.

### RISK OF NOT HAVING

Poor or cheap peelable can blister, melt, tear or simply set like concrete during assembly so that the peelable does not peel / does not work.



## 14 No x-outs accepted

### BENEFITS

No partial assembly means improved efficiency for the customer.

### RISK OF NOT HAVING

Special set-ups are necessary for each panel with a defect, and if the x-outs are not clearly marked or not segregated from the main delivery, there is a risk of assembling a known bad board; waste of components and time.

